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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s): Krishnaswamy Ramkumar, Kaichiu Wong and Venuka Jayatilaka Group Art Unit:

Filed: 08/09/00

Examiner:

Serial No.:

Title: PROCESS FOR REDUCING LEAKAGE IN AN INTEGRATED CIRCUIT WITH SHALLOW TRENCH ISOLATED ACTIVE AREAS

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
MAA	A	5,468,676	11/21/95	Madan	437	70	06/17/94
MAA	B	5,858,858	01/12/99	Park et al.	438	424	10/11/96
MAA	C	5,910,018	06/08/99	Jang	438	425	02/24/97
MAA	D	5,950,096	09/07/99	Huang et al.	438	443	09/22/97
MAA	E	5,960,299	09/28/99	Yew et al.	438	424	09/28/99
MAA	F	5,994,178	11/30/99	Wu	438	221	12/31/97
MAA	G	6,001,706	12/14/99	Tan et al.	438	424	12/08/97
MAA	H	6,004,864	12/21/99	Huang et al.	438	433	02/25/98
MAA	I	6,005,279	12/21/99	Luning	257	510	12/18/97
MAA	J	6,013,561	01/11/00	Jang et al.	438	452	10/30/97
MAA	K	6,020,251	02/01/00	Peng et al.	438	425	04/20/98
MAA	L	6,033,968	03/07/00	Sung	438	424	03/29/99
MAA	M	6,037,018	03/14/00	Jang et al.	427	579	07/01/98

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
	N						Yes	No
	O							
	P							
	Q							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	
	S	
Examiner	Matthew Andersen	
	Date Considered 3/18/02	

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.